

Second Ordinance, DTU (M.Tech) M.B.A.

रजिस्ट्री सं. डी.एल.—33002/99

भारत सरकार
GOVERNMENT OF INDIA

REGISTERED No.

दिल्ली



राजपत्र

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भाग—IV

PART—IV

राष्ट्रीय राजधानी राज्य क्षेत्र दिल्ली सरकार

GOVERNMENT OF THE NATIONAL CAPITAL TERRITORY OF DELHI

वित्त (कर एवं स्थापना) विभाग

अधिसूचना

दिल्ली, 2 फरवरी, 2010

सं. फा. 3(9)/वित्त (क. एवं. स्था.)/2009-10/जेएसएफ/66.—दिल्ली मूल्य संवर्धित कर नियमावली, 2005 के नियम 47 के साथ पठित दिल्ली मूल्य संवर्धित कर अधिनियम, 2004 (2005 का दिल्ली अधिनियम 3) की धारा 66 की उप-धारा (2) के खंड (क) द्वारा प्रदत्त शक्तियों का प्रयोग करते हुए, राष्ट्रीय राजधानी क्षेत्र दिल्ली के उपराज्यपाल, मूल्य संवर्धित कर आयुक्त, राष्ट्रीय राजधानी क्षेत्र दिल्ली सरकार की उक्त अधिनियम के प्रशासन में सहायता करने के लिए निम्नलिखित अधिकारियों को पदग्रहण की तिथि से नियुक्त करते हैं, अर्थात् :—

| क्रम संख्या | अधिकारी का नाम (श्री/श्रीमती/कु.) | पदनाम |
|-------------|-----------------------------------|---------------------------|
| 1. | आर. सी. गुप्ता | मूल्य संवर्धित कर अधिकारी |
| 2. | ब्रज मोहन | मूल्य संवर्धित कर अधिकारी |

राष्ट्रीय राजधानी क्षेत्र दिल्ली के उपराज्यपाल के आदेश से तथा उनके नाम पर,

अजय कुमार गर्ग, संयुक्त सचिव

FINANCE (T & E) DEPARTMENT

NOTIFICATION

Delhi, the 2nd February, 2010

No. F. 3(9)/Fin. (T & E)/2009-10/JSFin./66.—In exercise of the power conferred by clause (a) of sub-section (2) of Section 66 of the Delhi Value Added Tax Act, 2004 (Delhi Act 3 of 2005), read with rule 47 of the Delhi Value Added Tax rules, 2005, the Lt. Governor of the National Capital Territory of Delhi is pleased to appoint the following officers, with effect

| (1) | (2) | (3) | (4) | (5) | (6) | (7) | (8) |
|---------------|------|---------------|-----|-----|------|------|-----------|
| Semi Semes | ख | 2 कोर थियोरी | 2 | 50 | 100 | 150 | 100 20 |
| | | का कोर्स वर्क | | | | | |
| | | 1 प्रयोगशाला | 1 | 30 | 70 | 100 | |
| I | ग | स्वयं अध्ययन | - | 50 | - | 50 | 100 20 |
| | | ओपन एरिया | | | | | |
| | | संमीनार-1 | | | | | |
| II | घ | इलेक्ट्रिकल | 3 | 50 | 100 | 150 | 100 20 |
| | | थियोरी एवं | | | | | |
| | | कोर का कोर्स | | | | | |
| III | ङ | वर्क | | | | | 100 20 |
| | | 1. प्रयोगशाला | 1 | 30 | 70 | 100 | |
| | | कोर्स वर्क 2 | 2 | 50 | 100 | 150 | |
| IV | च | इलेक्ट्रिकल | | | | | 100 20 |
| | | थियोरी | | | | | |
| | | 1. प्रयोगशाला | 1 | 30 | 70 | 100 | |
| Total Cred | इ | लघु | | 50 | | 50 | 100 20 |
| | | परियोजना-1 | | | | | |
| | | 2 इलेक्ट्रिकल | 2 | 50 | 100 | 150 | |
| IV | फ | थियोरी का | | | | | 100 20 |
| | | कोर्स वर्क | | | | | |
| | | 2. प्रयोगशाला | 2 | 30 | 70 | 100 | |
| Total Cred | बृहद | स्वयं अध्ययन | - | - | 100 | 100 | 100 20 |
| | | ओपन अध्ययन | | | | | |
| | | ओपन एरिया | | | | | |
| IV | फ | संमीनार-1) | | | | | 100 20 |
| | | लघु | | | 200 | 200 | |
| | | परियोजना-II | | | | | |
| Total Cred | बृहद | बृहद परियोजना | - | - | 200 | 200 | 100 20 |
| | | भाग-I | | | | | |
| | | बृहद परियोजना | - | - | 1000 | 1000 | |
| Total Cred | कुल | भाग-II | | | | | 40 80 |
| | | | | | | | |
| | | | | | | | |

DELHI TECHNOLOGICAL UNIVERSITY

NOTIFICATION

Delhi, the 2nd February, 2010

No. F. DTU/ORG/Notification/04(1)/2009/114.- In exercise of the powers conferred by sub-section 32 of the Delhi Technological University Act, 2009 (Delhi Act 6 of 2009), the Board of Management, Delhi Technological University, hereby makes Ordinance-2 relating to Admission to Courses of Study, Conduct and Evaluation of for Post-Graduate Programs leading to Master of Technology (M.Tech.) Degree.

1. **Short title and commencement.**—(a) These Ordinance may be called the Delhi Technological (Second) Ordinance, 2009.

(b) They shall come into force with effect from the date of meeting of the Board of Management i.e.

2. Definitions.—

- (a) Academic Programme/Programme shall mean a programme of courses and/or any other component leading to the Master's degree in Technology.
- (b) An Academic Year is a period of nearly 12 months devoted to requirement specified in the school of teaching and the related examination.
- (c) Course means a component of the academic programme, carrying a distinctive code no. and specific credit assigned to it.
- (d) Board of Studies (BoS) shall mean the Board of Studies of the concerned Department/School of Studies.
- (e) Examiners shall mean the teacher who is in employment of University or who is not under employment of the University or its maintained colleges and has been assigned the task of conducting examination of any of the program of the University.
- (f) Examination Committee shall mean a Committee constituted for the purpose of maintaining the standards of examination.
- (g) Mid-term Examination shall mean the examination conducted during the semester.
- (h) Semester System A Programme wherein each academic year is apportioned into two semesters.
- (i) Student shall mean a person admitted to the Department/School of Studies/Constituent Colleges of the University for any of the academic programs to which this Ordinance is applicable.
- (j) University shall mean Delhi Technological University.
- (k) Paper Setter shall mean a person who has been assigned the work of paper setting, may be the teacher of the University or outside teacher who is not under employment of the University.
- (l) End-term Examination shall mean the examination conducted at the end of the semester.
- (m) Registration shall mean registration for a course/courses in a semester at the start of the semester of any program of the University to which this ordinance is applicable.
- (n) Unfair Means Committee shall mean a Committee for examining the cases of unfair means and for recommending punishment, if any, in such cases.

3. Degree.—The University shall hold examinations for the academic programs, leading to a Master's Degree in Technology, as are approved by the Academic Council and as it may notify from time to time for awarding Masters' Degree in Technology as per the prescribed Schemes of Teaching and Examinations and Syllabi.

4. Program Content and Duration.—

- (i) Each of the Fulltime M.Tech Program shall be of two years duration and shall consist of four semesters. For the part-time candidates the program duration shall be three years comprising of 6 semesters.
- (ii) The maximum span period for the full time M.Tech program shall be four years and for the part-time programs shall be five years.

5. Semester.—An academic year shall be apportioned in two semesters. Each semester shall consist of approximately 16 weeks duration including the examination period.

6. Examination of the University.—

- (i) The University shall conduct mid-term and end-semester examinations for all the courses offered for its various M.Tech programs in each semester.

